

L Number	Hits	Search Text	DB	Time stamp
-	47072	look\$1up adj table	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/19 11:59
-	32633	((look\$1up adj table) and @ad<20000602	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/19 12:00
-	0	09447085.an.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/19 11:51
-	203	((look\$1up adj table) and @ad<20000602) and (vhdl or verilog)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/19 12:00
-	37	((((look\$1up adj table) and @ad<20000602) and (vhdl or verilog)) and (eda or (electronic adj design adj automation)))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/19 12:00
-	11	(((((look\$1up adj table) and @ad<20000602) and (vhdl or verilog)) and (eda or (electronic adj design adj automation))) and (read and write)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/19 12:02
-	49322	((look\$1up adj table) or LUT)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/19 11:59
-	34135	((((look\$1up adj table) or LUT)) and @ad<20000602	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/19 12:00
-	209	(((((look\$1up adj table) or LUT)) and @ad<20000602) and (vhdl or verilog)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/19 12:10
-	130	((((((look\$1up adj table) or LUT)) and @ad<20000602) and (vhdl or verilog)) and (eda or (electronic adj design adj automation) or (circuit with design))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/19 12:10
-	201	((((((look\$1up adj table) or LUT)) and @ad<20000602) and (vhdl or verilog)) and (eda or (electronic adj design adj automation) or (circuit with design) or (circuit model\$5))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/19 12:02
-	134	((((((look\$1up adj table) or LUT)) and @ad<20000602) and (vhdl or verilog)) and (eda or (electronic adj design adj automation) or (circuit with design) or (circuit with model\$5))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/19 12:02
-	65	((((((((look\$1up adj table) or LUT)) and @ad<20000602) and (vhdl or verilog)) and (eda or (electronic adj design adj automation) or (circuit with design) or (circuit with model\$5))) and (read and write)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/19 12:11
-	13	((((((((look\$1up adj table) or LUT)) and @ad<20000602) and (vhdl or verilog)) and (eda or (electronic adj design adj automation) or (circuit with design) or (circuit with model\$5))) and (read and write)) and (valid\$3 with bit)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/19 12:11
-	269	(((((look\$1up adj table) or LUT)) and @ad<20000602) and (vhdl or verilog or hdl)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/19 12:09
-	172	((((((look\$1up adj table) or LUT)) and @ad<20000602) and (vhdl or verilog or hdl)) and (eda or (electronic adj design adj automation) or (circuit with design) or (circuit with model\$5))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/19 12:10

-	129	(((((look\$1up adj table) or LUT)) and @ad<20000602) and (vhdl or verilog or hdl)) and (eda or (electronic adj design adj automation) or (circuit with design) or (circuit with model\$5))) and (read\$3 and writ\$3)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/19 12:11
-	15	(((((look\$1up adj table) or LUT)) and @ad<20000602) and (vhdl or verilog or hdl)) and (eda or (electronic adj design adj automation) or (circuit with design) or (circuit with model\$5))) and (read\$3 and writ\$3)) and (valid\$3 with bit)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/02/19 12:11